



E-SERIES · DATA PROCESSING UNIT

E1L

200G DPU with an open standards Linux/DPDK Data Plane for the Hyperscale Cloud and Edge

Bringing Software-Defined Infrastructure to Every Socket.

200 Gbps
Accelerated networking

22
Arm Neoverse N2 cores

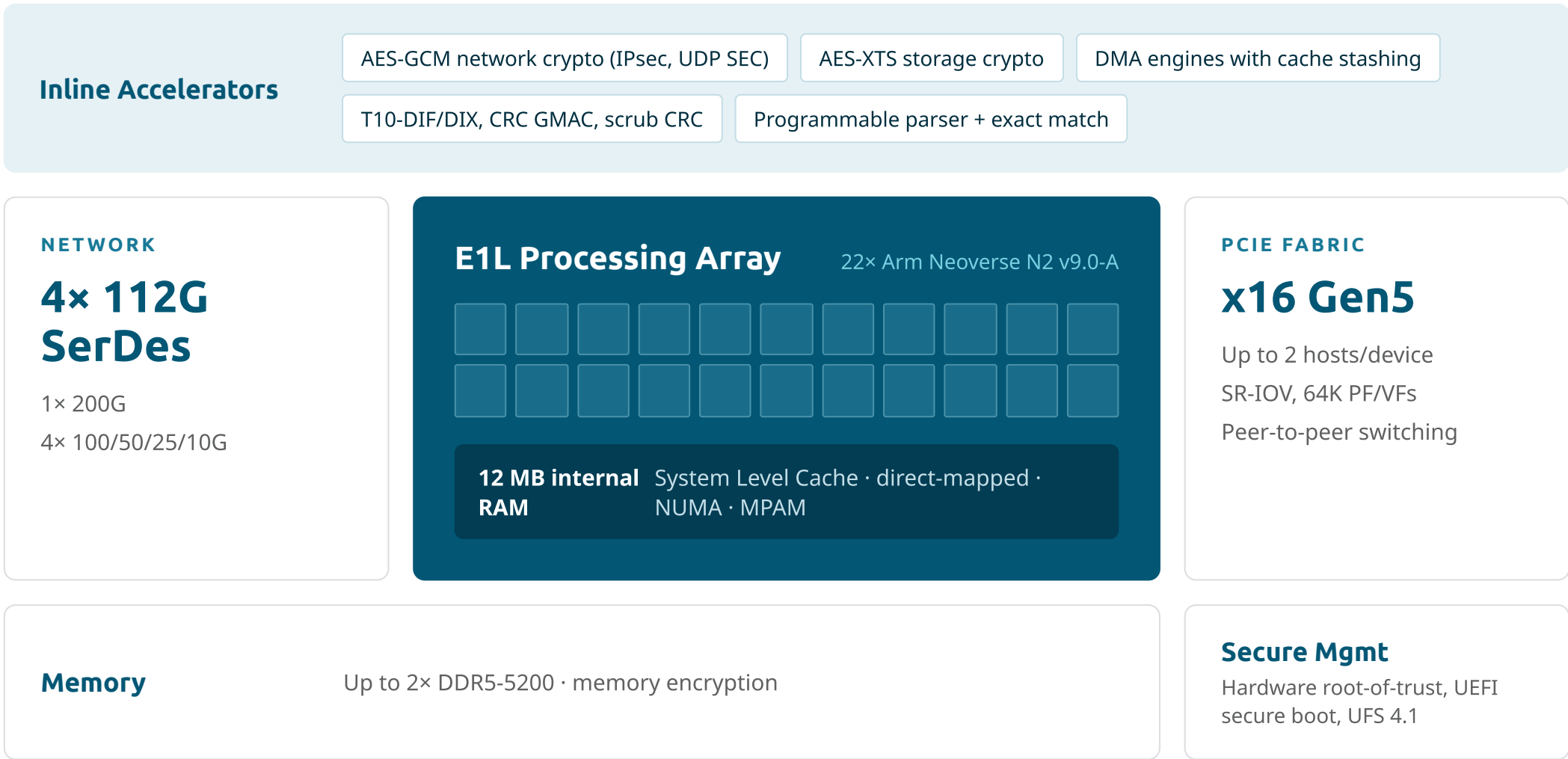
12 MB
Internal RAM (SLC or direct-mapped)

16
PCIe Gen5 lanes
Plus 4 management lanes

INSIDE THE SOC

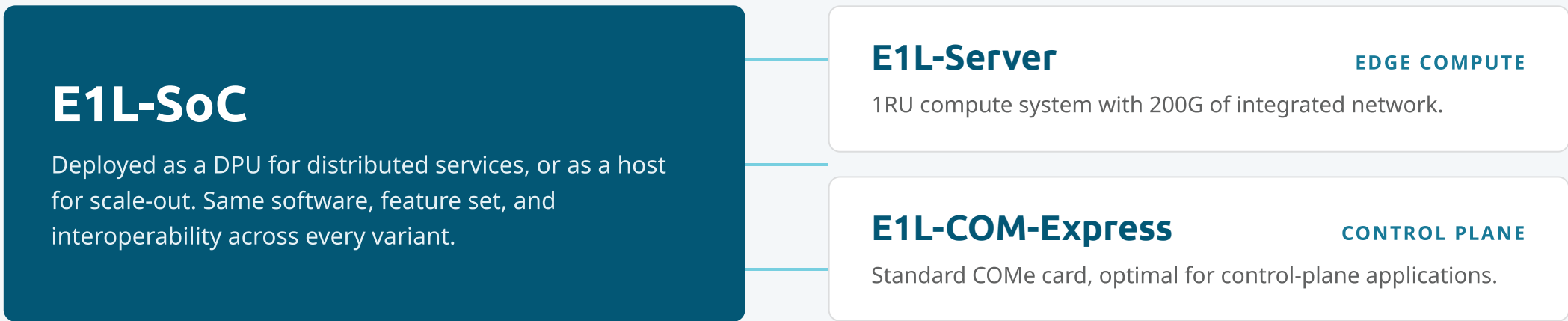
Every Packet Through the Processing Array

A high-speed coherent interconnect gives unified, coherent control and data planes, while supporting large control structures.



E1L SYSTEMS

One SoC, Three Form Factors



BUILT FOR

- Cloud and Data Center Infrastructure

Accelerated networking, wire-speed inline security, and storage offloads with a customizable PCIe fabric
- Open SDN and SDS Development

Native XDP and DPDK/SPDK support for in-kernel and user-space data planes
- Edge Cloud Host

Best-in-class performance per watt and TCO for memory-intensive, scale-out workloads

OPEN SOFTWARE

Arm SystemReady. Unmodified Linux distros.

- Debian
- Ubuntu
- SONiC + DASH
- Lightbits LightOS™
- DPDK / SPDK
- XDP
- Linux Netdev
- VirtIO
- NVMe/TCP
- RoCEv2
- Virtual Switch
- UEFI + TF-A



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